

#### ABSTRACT OF THE DISCLOSURE

Methods and apparatuses for signal line termination with minimum current flowing through a termination node. In one embodiment, a voltage regulator is connected between a termination node and a system potential reference plane, which is typically system ground or  $V_{ss}$ , to regulate a terminating potential, which can be used as a reference potential for the input buffers to receive the signals. The voltage regulator continuously adjusts the reference potential that will be used by the signal line input buffers to minimize the current flow into and out of the termination node of the signal lines.